

DESCRIPTION

The high power HVV1012-060 device is a high voltage silicon enhancement mode RF transistor designed for L-Band pulsed avionics applications operating over the frequency range from 1025MHz to 1150MHz.

FEATURES

High Power Gain
Excellent Ruggedness
48V Supply Voltage

ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Value	Unit
V _{DSS}	Drain-Source Voltage	95	V
V _{GS}	Gate-Source Voltage	10	V
I _{DSX}	Drain Current	4	A
P _D ²	Power Dissipation	625	W
T _S	Storage Temperature	-65 to +200	°C
T _J	Junction Temperature	200	°C

THERMAL CHARACTERISTICS

Symbol	Parameter	Max	Unit
θ_{JC}^1	Thermal Resistance	0.28	°C/W

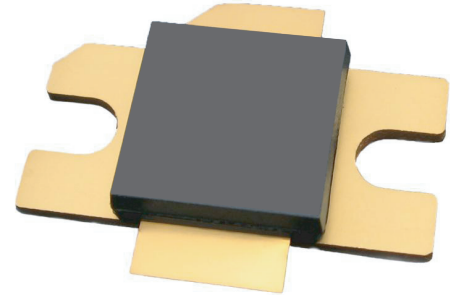
ELECTRICAL CHARACTERISTICS

Symbol	Parameter	Conditions	Typ	Units
V _{BR(DSS)}	Drain-Source Breakdown	V _{GS} =0V, I _D =2mA	102	V
I _{DSS}	Drain Leakage Current	V _{GS} =0V, V _{DS} =48V	<50	μ A
I _{GSS}	Gate Leakage Current	V _{GS} =5V, V _{DS} =0V	<1	μ A
G _p ¹	Power Gain	P _{OUT} =60W, F=1025, 1150MHz	23	dB
IRL ¹	Input Return Loss	P _{OUT} =60W, F=1025, 1150MHz	9	dB
η_D^1	Drain Efficiency	P _{OUT} =60W, F=1025, 1150MHz	52	%
PD ¹	Pulse Droop	P _{OUT} =60W, F=1025, 1150MHz	<0.3	dB

¹Under Pulse Conditions: Pulse Width = 10 μ sec, Pulse Duty Cycle = 1% at V_{DD} = 48V, I_{DQ} = 25mA

²Rated at T_{CASE} = 25°

PACKAGE



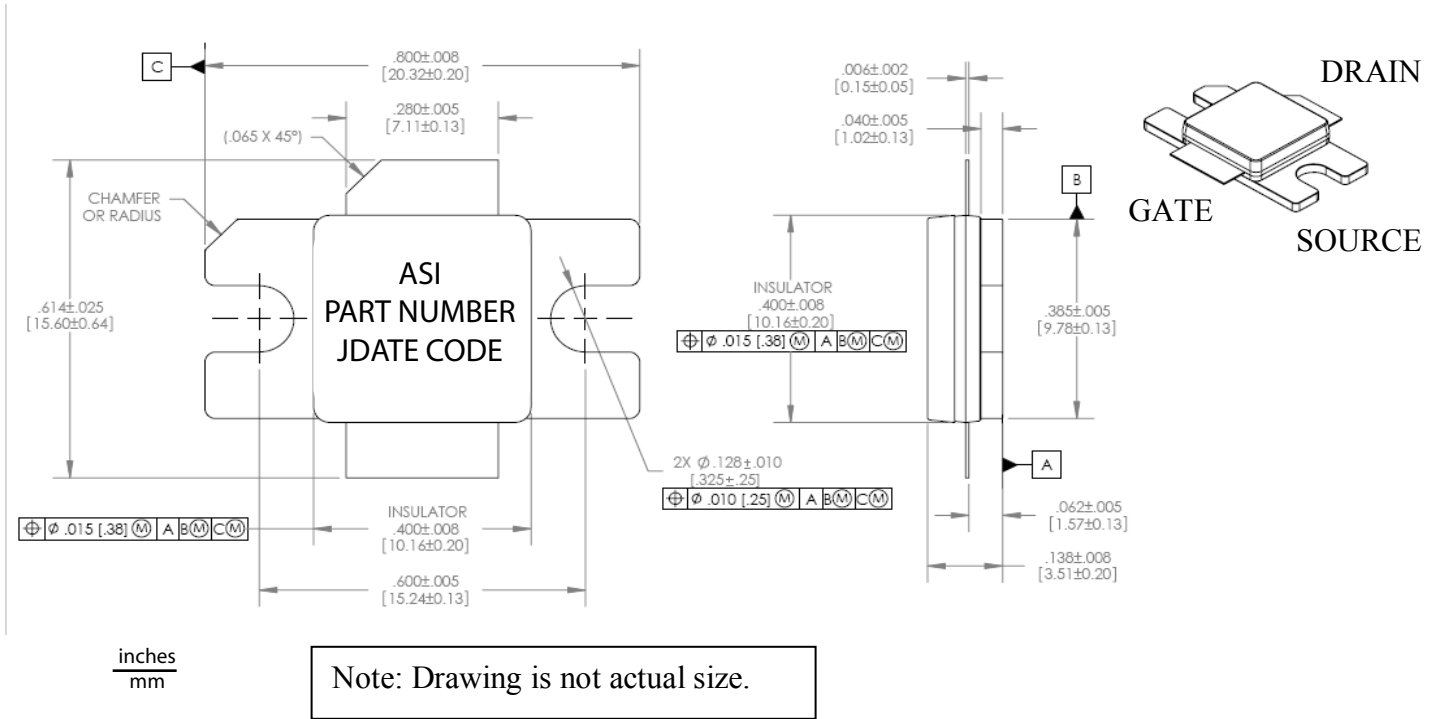
The device resides in a two-lead metal flanged package with liquid crystal polymer lid. The HV400 package style is qualified for gross leak test – MIL-STD-883, Method 1014.

RUGGEDNESS

The HVV1012-060 device is capable of withstanding an output load mismatch corresponding to a 20:1 VSWR at rated output power and operating voltage across the frequency band of operation.

Symbol	Parameter	Test Condition	Max	Units
LMT ¹	Load Mismatch Tolerance	P _{OUT} = 60W F = 1150MHz	20:1	VSWR

PACKAGE DIMENSIONS



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