

# STEP RECOVERY DIODE CHIP

**DESCRIPTION:**

The **ASI 5082-0008** is a Silicon Oxide Passivated SRD Chip Designed for hybrid local oscillator Applications.

**FEATURES:**

- Gold contact metalization
- Passivated Chip

**MAXIMUM RATINGS**

<b>I</b>	100 mA
<b>V</b>	15 V
<b>P<sub>DISS</sub></b>	100 mW @ T <sub>C</sub> = 25 °C
<b>T<sub>J</sub></b>	-60 °C to +200 °C
<b>T<sub>STG</sub></b>	-65 °C to +200 °C
<b>T<sub>SOLD</sub></b>	t ≤ 1.0 MINUTE +310 °C

**PACKAGE STYLE CHIP**

CHIP SIZE CODE	CHIP SIZE	PAD SIZE CODE	PAD DIA
X	S	Y	D
1	15	1	1T02
2	20	2	22
3	30		
7	70		

CHIP = 0.020 X 0.020 X 0.011 INCHES  
 TOP BOND CONTACT GOLD = 0.0025 INCHES  
 BOTTOM CONTACT GOLD (FULL AREA)

**CHARACTERISTICS** T<sub>C</sub> = 25 °C

SYMBOL	TEST CONDITIONS	MINIMUM	TYPICAL	MAXIMUM	UNITS
V <sub>B</sub>	I <sub>R</sub> = 10 μA	15			V
C <sub>J</sub>	V <sub>R</sub> = 10 V			0.38	pF
R <sub>S</sub>	I <sub>F</sub> = 20 mA		0.8		Ohms
τ	I <sub>F</sub> = 10 mA      I <sub>R</sub> = 6.0 mA		10		μS
t <sub>t</sub>	C <sub>L</sub> = 100 pc		60		pS